

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	CIRCUIT AND METHOD OF REDUCING NOISE IN AN RF POWER AMPLIFIER							
Application Number :		10/743217						
Confirmation Number:		7655						
First Named Applicant:		Alan Westwick						
Attorney Docket Number:		SIL.P0064						
Art Unit:		2817						
Examiner:								
Search string:		(6462620 or 6549071).pn						
US Patent Documents								
Note: Applicant is not required to submit a paper copy of cited US Patent Documents								
Init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass	
	1	6462620	2002-10-08	Dupuis et al.				
	2	6549071	2003-04-15	Paul et al.				
Remarks								
Note: Remarks are not for responding to an office action.								
Applicants would also like to make the Examiner aware of the following commonly owned co-pending patent application: Serial No. 10/743,221, filed on 12/22/2003, attorney docket number SIL.P0063, entitled "CIRCUIT AND METHOD OF ESTABLISHING DC BIAS LEVELS IN AN RF POWER AMPLIFIER", by Alan L. Westwick et al. (pending).								
Signature								
Examiner Name				Date				